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**\*\* CONTINUING DATA \*\*\*\*\*** *TD Yes*

This application is a 371 of PCT/JP00/05044 07/28/2000

**\*\* FOREIGN APPLICATIONS \*\*\*\*\*** *TD Yes*

JAPAN 11/228852 08/12/1999  
 JAPAN 11/230875 08/17/1999  
 JAPAN 11/266932 09/21/1999  
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 36	TOTAL CLAIMS 33	INDEPENDENT CLAIMS 9
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>fy</i>				

**ADDRESS**  
23373

**TITLE**  
MULTILAYER PRINTED WIRING BOARD, SOLDER RESIST COMPOSITION, METHOD FOR MANUFACTURING MULTILAYER PRINTED WIRING BOARD, AND SEMICONDUCTOR DEVICE

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